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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/068,650	02/05/2002	John S. Packer	ADPT1053	8107

7590

02/25/2005

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EXAMINER

PATEL, NIMESH G

ART UNIT

PAPER NUMBER

2112

DATE MAILED: 02/25/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/068,650	PACKER, JOHN S.	
	Examiner	Art Unit	
	Nimesh G Patel	2112	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 November 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-35 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 13-32 is/are allowed.
- 6) ☒ Claim(s) 1-5, 12 and 33 is/are rejected.
- 7) ☒ Claim(s) 6-11, 34 and 35 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 February 2002 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-5, 12 and 33 are rejected under 35 U.S.C. 102(b) as being anticipated by PCI-to-PCI Bridge Architecture Specification, Revision 1.1, hereinafter referred to as PCI Bridge.

3. Regarding claim 1, PCI Bridge discloses a device comprising: a first I/O bus-interface circuit(Figure 1-2, Page 13); and an on-the-fly message manipulation circuit connected to said first I/O bus-interface circuit, wherein said on-the-fly message manipulation circuit sets on-the-fly a pre-selected sub-unit of a pre-selected message-unit of a message to a pre-selected state as said pre-selected message-unit is passed through said device(Page 21, Section 3.1.2.1.1).

4. Regarding claim 2, PCI Bridge discloses a device, wherein on-the-fly manipulation circuit further comprises: a message detector module comprising: an input coupled to said first I/O bus-interface circuit; and a message-detected line, wherein in response to information indicative of said message on said input, said message detector module generates an active signal on said message-detected line(Page 19, Section 3.1; It is inherent a configuration transaction is detected).

5. Regarding claim 3, PCI Bridge discloses a device, wherein said on-the-fly message manipulation circuit further comprises: a message-unit detector module having a message-unit detected line, wherein said message-unit detector module generates an active signal on said message-unit detected line upon detecting said pre-selected message-unit of said message(Page 21, Section 3.1.2.1.1).

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6. Regarding claim 4, PCI Bridge discloses a device, wherein said on-the-fly message manipulation circuit further comprises: a message-unit detector module having a message-unit detected line, wherein said message-unit detector module generates an active signal on said message-unit detected line upon detecting said pre-selected message-unit of said message(Page 21, Section 3.1.2.1.1).

7. Regarding claim 5, PCI Bridge discloses a device, wherein said on-the-fly message manipulation circuit further comprises: a message sub-unit state selection module coupled to said message-detected line and to said message-unit detected line, wherein said message sub-unit state selection module sets said pre-selected sub-unit of said pre-selected message-unit of said message to said pre-selected state after receiving said active signal on said message-detected line, and said active signal on said message-unit detected line(Page 21, Section 3.1.2.1.1).

8. Regarding claim 12, PCI Bridge discloses a device further comprising: a second I/O bus-interface circuit connected to said on-the-fly message manipulation circuit(Page 13, Figure 1-2).

9. Regarding claim 33, PCI Bridge discloses a method for configuring a pre-selected sub-unit of a message on-the-fly comprising: detecting said message using a hardware circuit(It is inherent a message is detected); detecting a pre-selected message-unit of said message using said hardware circuit; and configuring said pre-selected sub-unit of said pre-selected message-unit of said message to a pre-selected state using said hardware circuit as said pre-selected message-unit is passed through a device including said hardware circuit(Page 21, Section 3.1.2.1.1).

Allowable Subject Matter

10. Claims 13-32 are allowed. The Prior art does not show a SCSI manipulation circuit in a SCSI expander to manipulate a precompensation enable control bit on the fly.

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11. Claims 6-11 and 34-35 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nimesh G Patel whose telephone number is 571-272-3640. The examiner can normally be reached on M-F, 8:30-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark H Rinehart can be reached on 571-272-3632. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nimesh G Patel
Examiner
Art Unit 2112

NP NP
February 23, 2005


Glenn A. Auve
Primary Patent Examiner
Technology Center 2100